

REMARKS

In an Office Action mailed on June 22, 2005, claims 1-8, 21-26, 29, 30, 34-36 and 40-42 were rejected under 35 U.S.C. § 112, second paragraph as being indefinite; and claims 8-14, 21-26, 29, 30, 34-36 and 40-42 were rejected under 35 U.S.C. § 102(e) as being anticipated by Taruishi.

Applicant requests withdrawal of the § 112, second paragraph rejections for at least the reason that the alleged defective language, i.e., the "triggering" language, is no longer present in the claims. In this regard, the "triggering" language was taken out in the last Reply filed by Applicant on April 21, 2005. Therefore, for at least the reason that the alleged basis for the § 112 rejections has already been corrected, withdrawal of the § 112, second paragraph rejections of claims 8-14, 21-26, 29, 30, 34-36 and 40-42 is requested.

The § 102 rejections are addressed below.

Rejections of Claims 8-12:

The method of independent claim 8 recites that in response to a read operation occurring over memory bus, data signals that are received directly from the memory bus into a memory controller are amplified.

Applicant requests the Examiner to consider the specific limitations of independent claim 8. In this regard, the latest Office Action appears to address only the previous claim limitations and does not discuss the claim limitations as of April 21, 2005, the date on which the last Reply was filed. In this regard, Taruishi only describes a set of amplifiers that amplify data signals that are received directly from a memory bus, i.e., amplifiers that are associated with the data input circuit 3. Therefore, it is clear from Taruishi that the disclosed amplifier activated in response to a write, a command that occurs in a write operation. This is in stark contrast to the limitations of amended independent claim 8 that make it clear that the reception of the data signals occur in response to a read operation.

Therefore, for at least the reason that Taruishi fails to teach or even suggest the limitations of amended independent claim 8, withdrawal of the § 102 rejection of this claim is

requested. Claims 9-12 are patentable for at least the reason that these claims depend from an allowable independent claim.

Rejections of Claims 21-23:

The memory controller of independent claim 21 includes amplifiers that, in response to a read operation occurring over a memory bus, amplifies data signals that are received directly from the memory bus.

See discussion of independent claim 8 above. In particular, Taruishi neither teaches nor even suggests the amplification of data signals that are received directly from a memory bus, as specifically recited in independent claim 21. The Office Action fails to discuss these specific claim limitations, and therefore, Applicant requests the Examiner to specifically consider and address the limitations when the considering the patentability of claim 21 in view of Taruishi.

Claims 22 and 23 are patentable for at least the reason that these claims depend from an allowable independent claim. Therefore, for at least the reasons that are set forth above, withdrawal of the § 102 rejections of claims 21-23 is requested.

Rejections of Claims 29 and 30:

The computer system of independent claim 29 includes a memory and a memory controller. The memory controller includes amplifiers.

See discussion of independent claims 8 and 21 above. Taruishi neither teaches nor even suggests the memory controller and likewise fails to teach or suggest the amplifiers that are part of a memory controller. Therefore, for at least any of these reasons, Taruishi fails to anticipate independent claim 29.

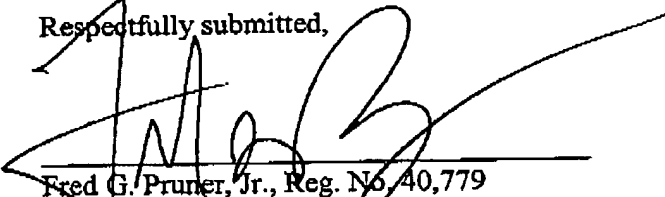
Claim 30 is patentable for at least the reasons that this claim depends from an allowable claim. Therefore, for at least the reasons that are set forth above, withdrawal of the § 102 rejections of claims 29 and 30 is requested.

CONCLUSION

In view of the foregoing, withdrawal of the §§ 102 and 112 rejections and a favorable action in the form of a Notice of Allowance are requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504 (ITL.0668US).

Respectfully submitted,

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